Electronic Acknowledgement Receipt				
EFS ID:	3404752			
Application Number:	10512405			
International Application Number:				
Confirmation Number:	4607			
Title of Invention:	HIGH RESISTANCE SILICON WAFER AND ITS MANUFACTURING METHOD			
First Named Inventor/Applicant Name:	Nobumitsu Takase			
Customer Number:	25227			
Filer:	Bradley J. Meier/Natherine Ordanza			
Filer Authorized By:	Bradley J. Meier			
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1 Issue Fee Payment (PTO-85B)	474082002800IF.pdf	91032	no	1	
		b2f079370d8df4bc4c0c5502fe606e853 5433f00			
Warnings:					
Information:					
2 Fee Worksheet (PTO-06)	fee-info.pdf	8505	no	2	
		6aa3e8b875a73ccaf1c74873348c463df edff3f2			
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If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

New International Application Filed with the USPTO as a Receiving Office

If a new international application is being filed and the international application includes the necessary components for an international filing date (see PCT Article 11 and MPEP 1810), a Notification of the International Application Number and of the International Filing Date (Form PCT/RO/105) will be issued in due course, subject to prescriptions concerning national security, and the date shown on this Acknowledgement Receipt will establish the international filing date of the application.